



The design center DMOS GmbH in Dresden develops customer specific mixed signal integrated circuits for smart power applications, mainly used in the automotive industry. Our ASICs and ASSPs are also used in smart home appliances or the "industry 4.0". DMOS GmbH owns a building, with excellent work and development conditions close to Dresden Main Station.

The DMOS GmbH is a subsidiary of the Elmos Semiconductor SE.

### Internship at the DMOS GmbH

*Verification and optimization of analog IC circuits in sub-micron technology*

#### During this internship/thesis the following tasks are to be solved:

- Familiarization with the Schematic Entry Tool (Synopsys)
- Familiarization with the Verification Tool (Synopsys)
- Familiarization with the technology to be used (Samsung)
- Development of verification tasks for a testbench
- Circuit verification
- Circuit optimization

#### Our requirements:

- Completed basic studies in electrical engineering, information systems engineering (or comparable).
- Experience in analog circuit design
- Experience in IC verification is an advantage

#### This is what you will benefit of:

- equitably remuneration
- Interesting and challenging tasks in an international environment
- Comprehensive introductory and training programs
- Competent co-workers
- Flexible working hours, possibility of mobile office, company events and social offers

**dmos**<sup>2</sup>  
member of the elmos group

Interested? Apply for it!

Send us your application as PDF file by mail.

We are looking forward to get to know you in person!

Contact:

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