



DMOS GmbH is a design center for the development of application-specific mixed-signal circuits for high-voltage applications used in the automotive industry as well as in building and industrial automation 4.0. DMOS GmbH has excellent development and working conditions in its own development and office building near Dresden's main railway station.

DMOS GmbH is a subsidiary of Elmos Semiconductor SE.

### Internship assignment at DMOS GmbH

*Analysis of standard DSP architectures for use in the eDSI data path*

#### The following tasks are to be solved as part of an internship:

- Analysis of DSP architectures in terms of instruction set, parallelism, memory interface, memory, compiler, etc.
- Setting up different DSP hardware descriptions and simulating simple calculation examples
- Implementation of the eDSI data path on a selected DSP architecture

#### You meet the following requirements

- Completed basic studies in electrical engineering, information systems technology (or comparable).
- Experience in the programming languages VHDL, System Verilog and C
- Experience in the Verilog language system and the UVM verification method is an advantage
- Experience in working with simulation tools
- Basic knowledge of digital design for ICs and FPGAs

#### Then you can expect

- Attractive remuneration
- Interesting and challenging tasks in an international environment
- Very good familiarization
- Competent colleagues who are on hand with help and advice
- Flexible working hours, company events and social offers

**dmos<sup>2</sup>**  
member of the elmos group

Interested? Apply for it!

Send us your application as PDF file by mail.

We are looking forward to get to know you in person!

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