



The design center DMOS GmbH in Dresden develops customer specific mixed signal integrated circuits for smart power applications, mainly used in the automotive industry. Our ASICs and ASSPs are also used in smart home appliances or the “industry 4.0”. DMOS GmbH owns a building, with excellent work and development conditions close to Dresden Main Station.

The DMOS GmbH is a subsidiary of the Elmos Semiconductor SE.

Internship/ Working student at the DMOS GmbH

Support for verification in digital design

Our interdisciplinary development teams develop highly complex automotive ASICs and standard products with maximum reliability. We are always looking for highly motivated students to join our design teams in the field of verification as part of an internship or as a working student.

During this internship/ working student activity the following tasks are to be solved:

- Familiarization and further development of our verification methods
- Development and implementation of verification strategies
- Documentation of the results

Our requirements:

- Completed basic studies in electrical engineering, information systems engineering (or comparable).
- Experience in the programming languages VHDL, Verilog and C
- Experience in the SystemVerilog language and the UVM verification method is an advantage
- Experience in the use of simulation tools
- Basic knowledge of digital design for ICs and FPGAs

This is what you will benefit of:

- equitably remuneration
- Interesting and challenging tasks in an international environment
- Comprehensive introductory and training programs
- Competent co-workers
- Flexible working hours, possibility of mobile office, company events and social offers

dmos⁺
member of the elmos group

Interested? Apply for it!

Send us your application as PDF file by mail.

We are looking forward to get to know you in person!

Contact:

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