



The design center DMOS GmbH in Dresden develops customer specific mixed signal integrated circuits for smart power applications, mainly used in the automotive industry. Our ASICs and ASSPs are also used in smart home appliances or the "industry 4.0". DMOS GmbH owns a building, with excellent work and development conditions close to Dresden Main Station.

The DMOS GmbH is a subsidiary of the Elmos Semiconductor SE.

### Internship at the DMOS GmbH

*Conversion of a circuit block with sub-groups into a layout suitable for mask generation, including final checks*

At DMOS GmbH, the IC layout department provides the data required for mask production for the manufacture of integrated circuits. This data is generated from the analog circuits using EDA tools, in which physical components are placed and routed accordingly. Finally, the data is checked for accuracy in terms of content and production-specific requirements.

#### During this internship/thesis the following tasks are to be solved:

- Familiarization with the layout tool (Cdesigner)
- Familiarization with the technology to be used (TSMC)
- Layout of a voltage divider
- Layout of a simple low-voltage circuit
- Creation of layouts for a block with sub-assemblies and additional, more complex circuits using high-voltage components

#### Our requirements:

- Completion of the first cycle of studies in electrical engineering or a comparable field
- Interest in graphics

#### This is what you will benefit of:

- equitably remuneration
- Interesting and challenging tasks in an international environment
- Competent co-workers
- Flexible working hours, company events and social offers

**dmos<sup>+</sup>**  
member of the elmos group

Interested? Apply for it!

Send us your application as PDF file by mail.

We are looking forward to get to know you in person!

Contact:

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