



The design center DMOS GmbH in Dresden develops customer specific mixed signal integrated circuits for smart power applications, mainly used in the automotive industry. Our ASICs and ASSPs are also used in smart home appliances or the “industry 4.0”. Since 2008 the DMOS GmbH owns a new building with excellent work and development conditions close to the Dresden Main Station. The DMOS GmbH is a subsidiary of the Elmos Semiconductor SE.

## Internship/thesis at DMOS GmbH

*Developing an Eclipse plugin (Java) for the UVM template generator*

For generation of SystemVerilog UVM test environments, DMOS GmbH uses a template-based generator. The edition of test sequences incorporates the IDE Eclipse. Subject of the project is to develop a plugin for Eclipse (using Java) which integrates the template generator into the Eclipse environment. Furthermore, the plugin should be extended in order to offer text formatting and autocomplete features for SystemVerilog.

### During this internship/thesis the following tasks are to be solved

- Familiarize with the existing project structure
- Develop a plugin for Eclipse that integrates the SystemVerilog template generator
- Integrate text formatting and autocomplete features

### Our Requirements

- Completed basic course of Electrical Engineering, Informatics or Information Systems Technology
- Experiences in Object Oriented Programming with Java
- Basic knowledge of SystemVerilog

### This is what you will benefit of

- Equitable Remuneration
- Interesting and challenging tasks in an international environment
- Competent co-workers
- Flexible working time, possibility of Mobile-Office, company and social events

**dmos**<sup>®</sup>  
member of the elmos group

Interested? Apply for it!

Send us your application as PDF file by mail.

We are looking forward to get to know you personally!

Contact:

**DMOS GmbH**  
Bergstraße 4  
D-01069 Dresden

Tel.: 0351 / 479 42 – 0  
dmos-bewerbungen@dmos2002.de

[www.dmos2002.de](http://www.dmos2002.de)